

MIPI D-PHY Datasheet

MIPI D-PHY Receiver for TSMC 40nm LP

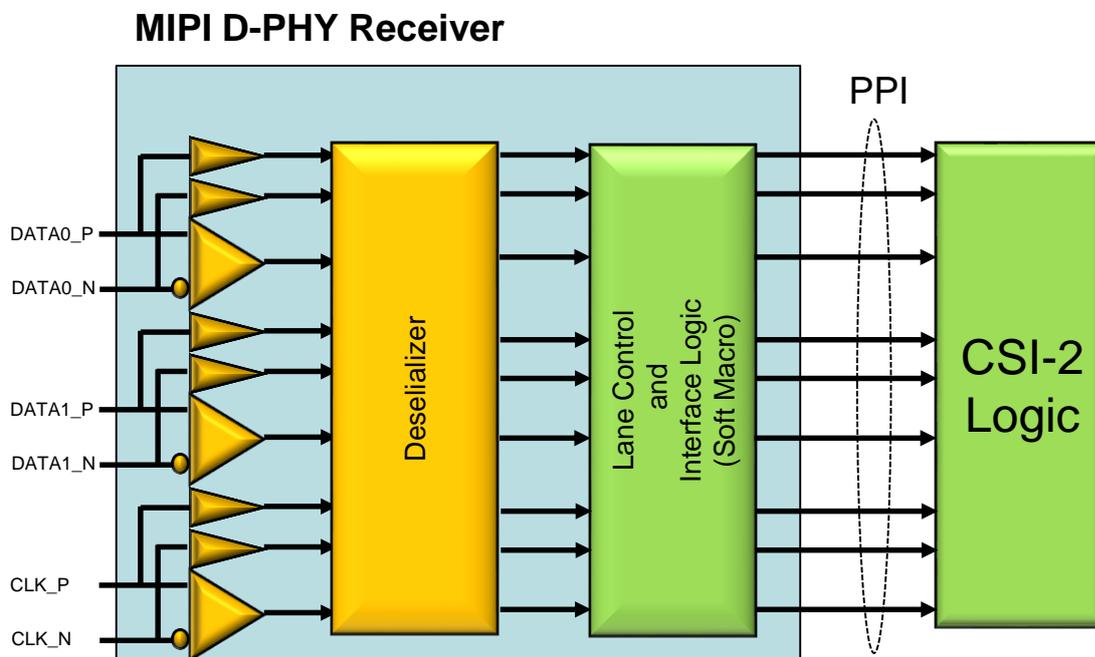
Overview

The Renesas MIPI D-PHY Receiver is useful 2 Data Channel receiver hard macro for CSI-2 of TSMC 40nm LP process.

Key Features

- Renesas MIPI D-PHY Receiver can be used for analog Receiver of following interface .
 - MIPI alliance Specification for D-PHY Version 2.1 15 December 2016.
 - MIPI alliance Specification for Camera Serial Interface 2 (CSI-2) Version 2.0 7 Dec 2016.
- Technology is TSMC 40nm LP 1p8M (4x2y1z) .
- Supply voltage can be applied 1.18V for core voltage, 1.8V for IO voltage.
- Maximum data rate of each channel is 1.0Gbps at High-speed mode.

Block Diagram



**These IPs are contract design IP. Please contact for detail.*

*Before purchasing or using any Renesas Electronics products listed herein, please refer to the latest product manual and/or data sheet in advance.